

***DM9162 RMII mode***  
***50Mhz clock output Configuration***  
***Application Notes V1.0***

***Technical Reference Manual***  
***Davicom Semiconductor, Inc***

When in RMI mode, DM9162 can generate a 50Mhz clock output when using 25Mhz crystal by add a pull-up resistor to pin 22, TXCLK.

**Hardware Configuration:**

50Mhz clock output from pin 22, TXCLK, is enabled when TXCLK has a pull-up resistor.

**Software Configuration:**

None

**Reference Circuit:**

